Austrochip
Workshop on Microelectronics

Austrochip 2014
22nd Austrian Workshop on Microelectronics
09 October 2014, Graz, Austria

Austrochip is an annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The workshop is a forum for discussion and contact between academia and industry.

Topics of the workshop include but are not limited to:

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, gigascale circuits, network-on-chip
- Embedded systems, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modelling, timing analysis, reliability simulation
- Emerging technologies, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- Case studies and prototyping
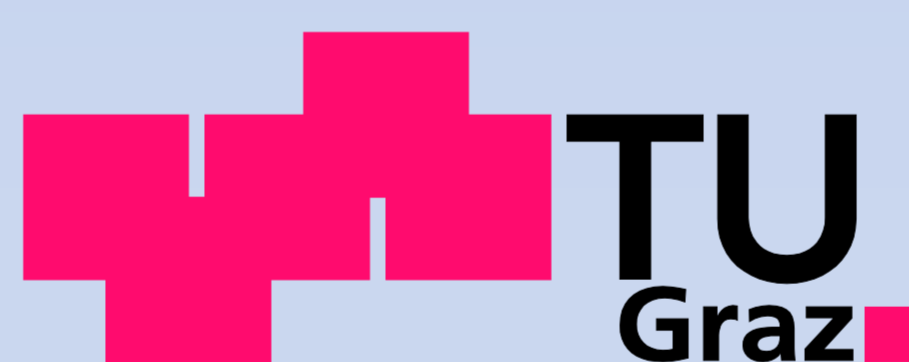
Invited speaker
Prof. Hubert Kaeslin (ETH Zurich)

Organization
Institute for Applied Information Processing and Communications (IAIK) at Graz University of Technology

Workshop venue
Hotel Weitzer Graz

www.austrochip.at   TU Graz

Important Dates

Submission Deadline
11 July 2014

Acceptance Notification
04 September 2014

Workshop
09 October 2014

Committee

Program chair
Michael Hutter, TU Graz

Publicity chairs
Stefan Mangard, TU Graz
KC Posch, TU Graz

Program committee
Mario Auer, TU Graz
Thomas Bauernfeind, Intel Austria
Bernd Deutschmann, TU Graz
Dieter Draxelmayr, Infineon Technologies
Michael Gebhart, NXP Semiconductors
Frank Gürkaynak, ETH Zurich
Jan Haase, TU Wien
Gerald Hilber, JKU Linz
Rainer Holzhaider, ams AG
Martin Horauer, FH Technikum Wien
Mario Huemer, JKU Linz
Michael Hutter, TU Graz
Nikolaus Kerö, Oregano Systems
Hans-Peter Kreuter, Infineon Technologies
Manfred Ley, FH Kärnten Villach
Wolfgang Mayerwieser, Dialog Semiconductor
Christian Netzberger, FH Joanneum Kapfenberg
Erwin Ofner, FH Kärnten Villach
Timm Ostermann, JKU Linz
Karl-Christian Posch, TU Graz
Peter Rössler, FH Technikum Wien
Kerstin Schneider-Hornstein, TU Wien
Peter Söser, TU Graz
Andreas Steininger, TU Wien
Johannes Sturm, FH Kärnten Villach
Gunter Winkler, TU Graz
Thomas Winkler, University of Klagenfurt
Johannes Wolkerstorfer, xface
Horst Zimmermann, TU Wien